

60V Input, 100mA Output Current, Low Quiescent Current LDO

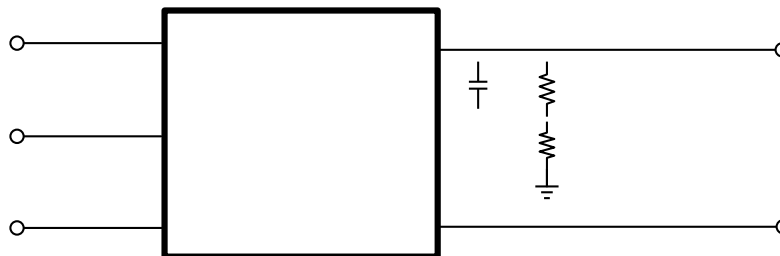
FEATURES

DESCRIPTION

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APPLICATIONS

TYPICAL APPLICATION



REVISION HISTORY

DEVICE ORDER INFORMATION

2UGHUDH0HF	2XWSX 9ROWD	3DFNDJ	3DFND 0DUNL	3,16	06/	7UDQVSRUW (C 4XDQWLWL

PIN CONFIGURATION



PIN FUNCTIONS

EMSOP-8L/SCT71601A00Q/ SCT71601FxxQ:

1 \$ 0 (	1 \$ 0 (	3 , 1) 8 1 & 7 , 2 1
		Ø
	%	

RECOMMENDED OPERATING CONDITIONS

3 \$ 5 \$ 0 (7 (5	' () , 1 , 7 , 2 1	0 , 1	0 \$;	8 1 , 7
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6 & 7 4 6 H U L H V

ABSOLUTE MAXIMUM RATINGS

3\$5\$0(7(5	'(), 1, 7, 2 1	0, 1	0\$;	8 1, 7

THERMAL INFORMATION

ELECTRICAL CHARACTERISTICS

6 < 0 % 2	3 \$ 5 \$ 0 (7 (5	7 (6 7 & 2 1 ' , 7 , 2 1	0 , 1 7 < 3 0 \$;	8 1 , 7
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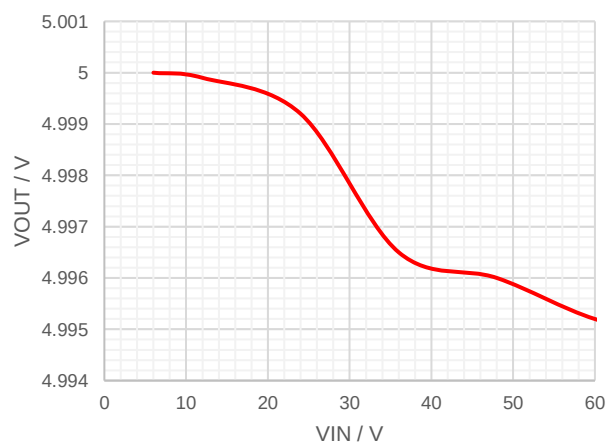
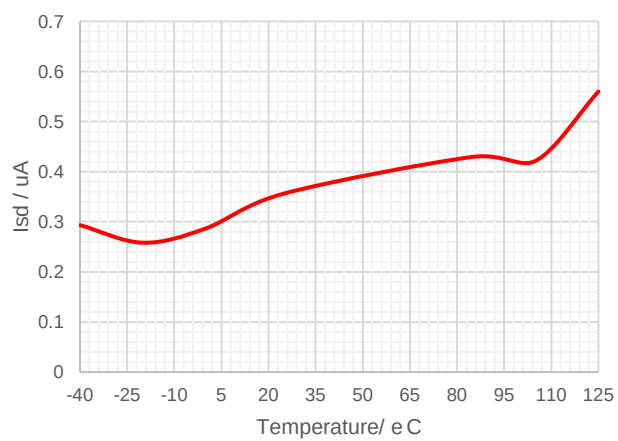
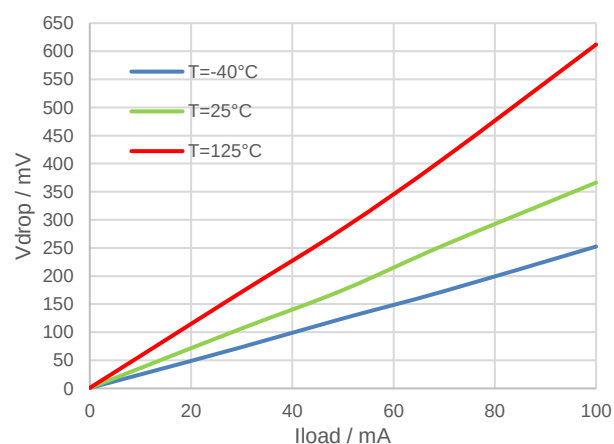
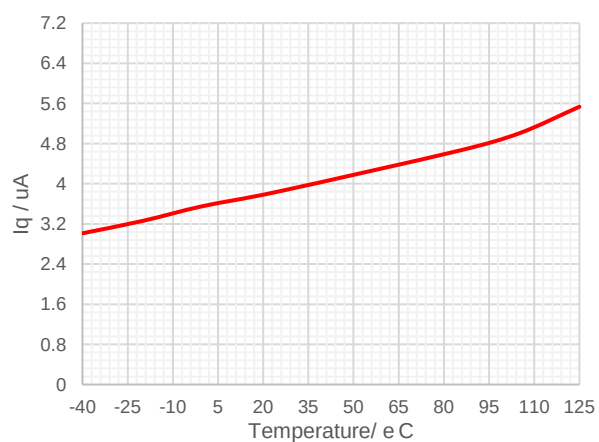
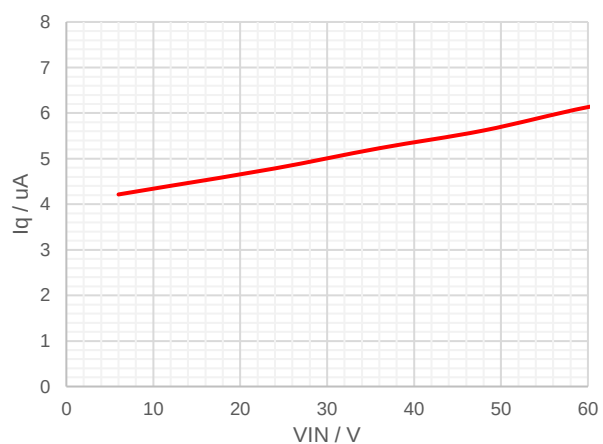
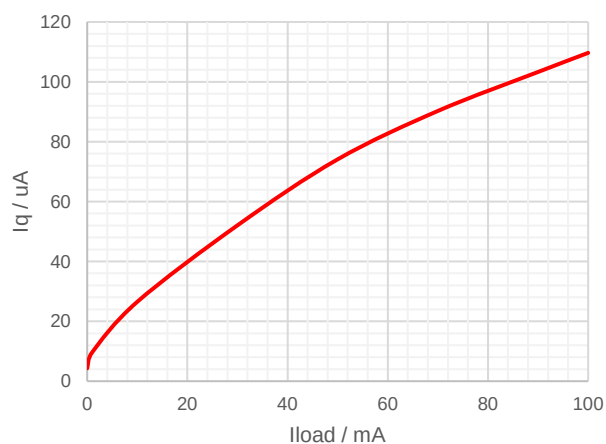
3RZHU 6XSSO\

[illegible]

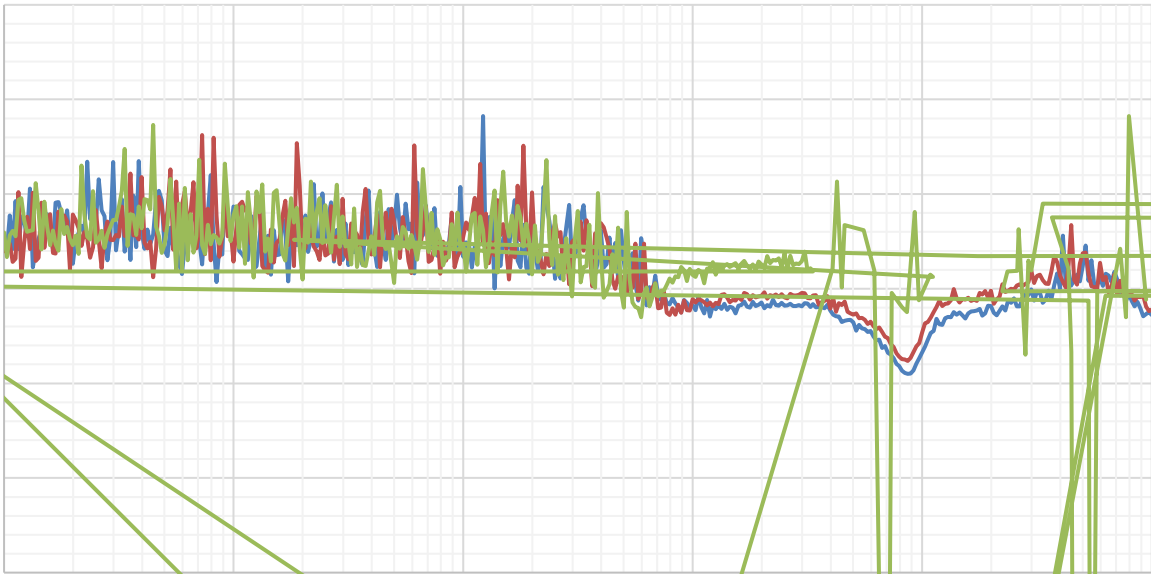
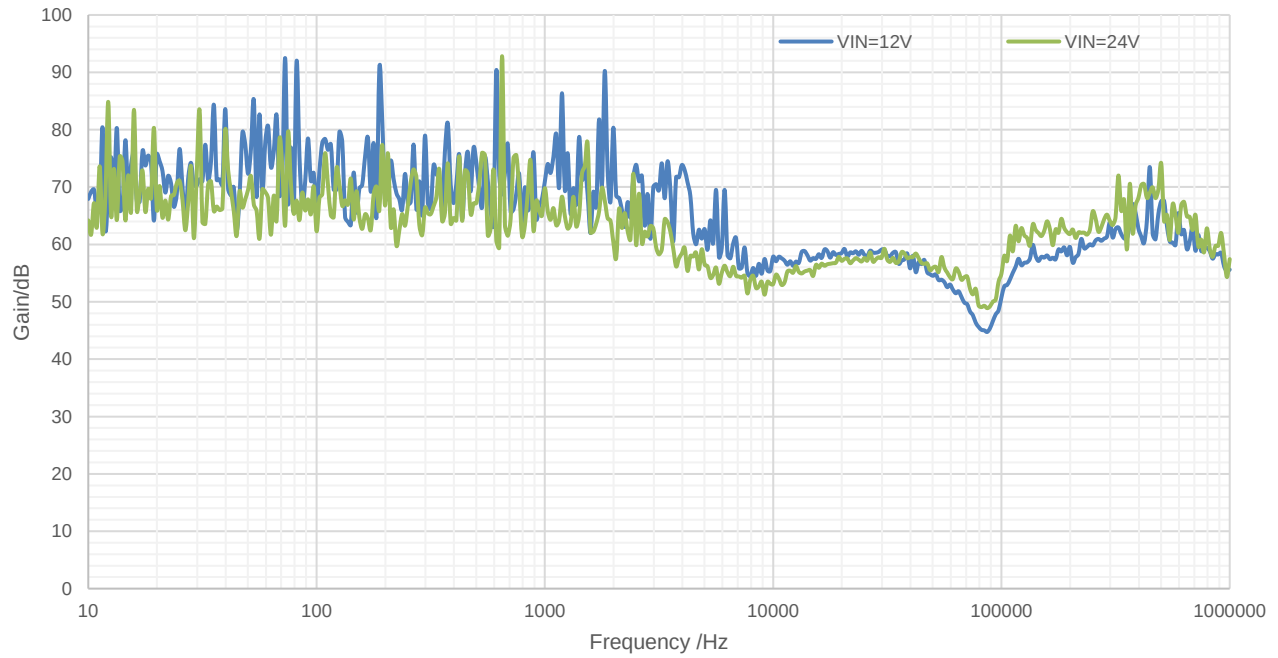
5HJXODWHG 2XWSXW 9ROWDJH DQG &XUUHQW

(QDEOH DQVWDRUWXS

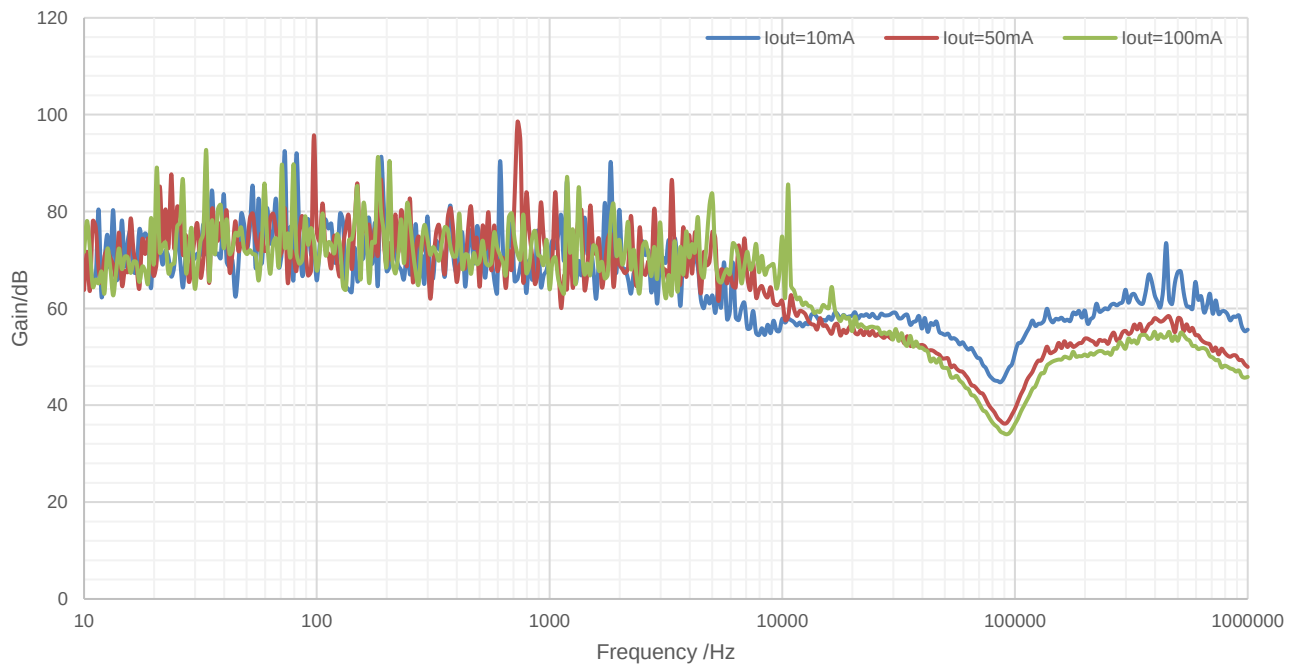
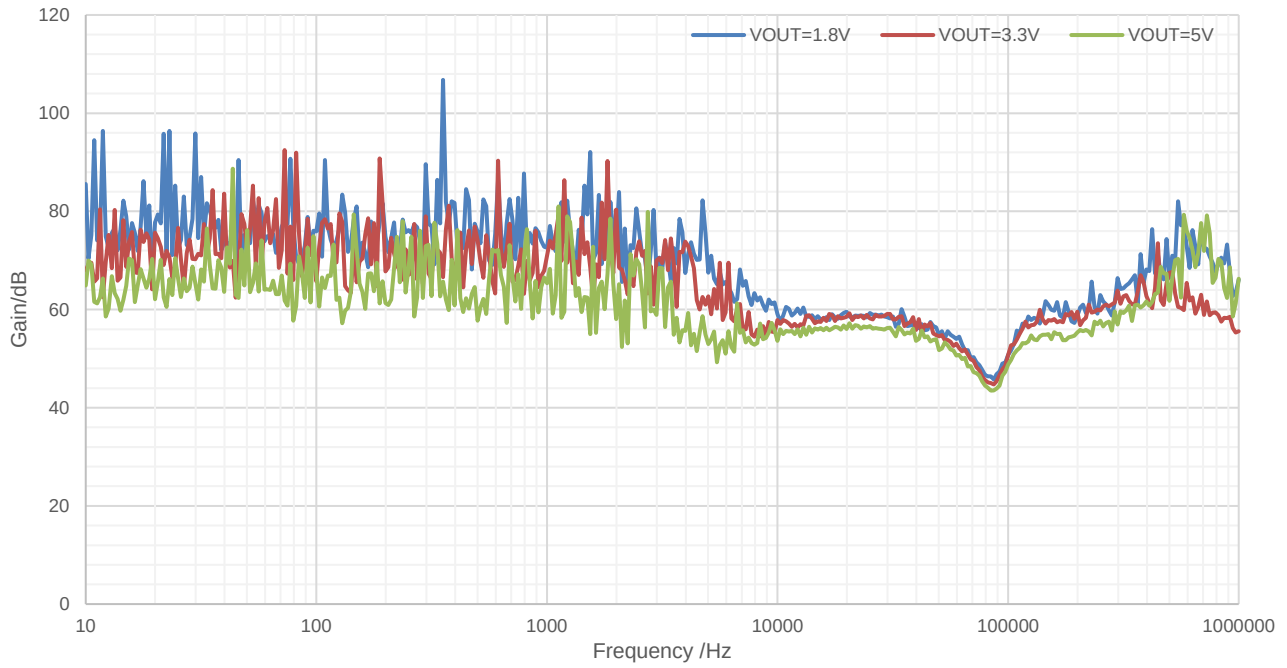
TYPICAL CHARACTERISTICS



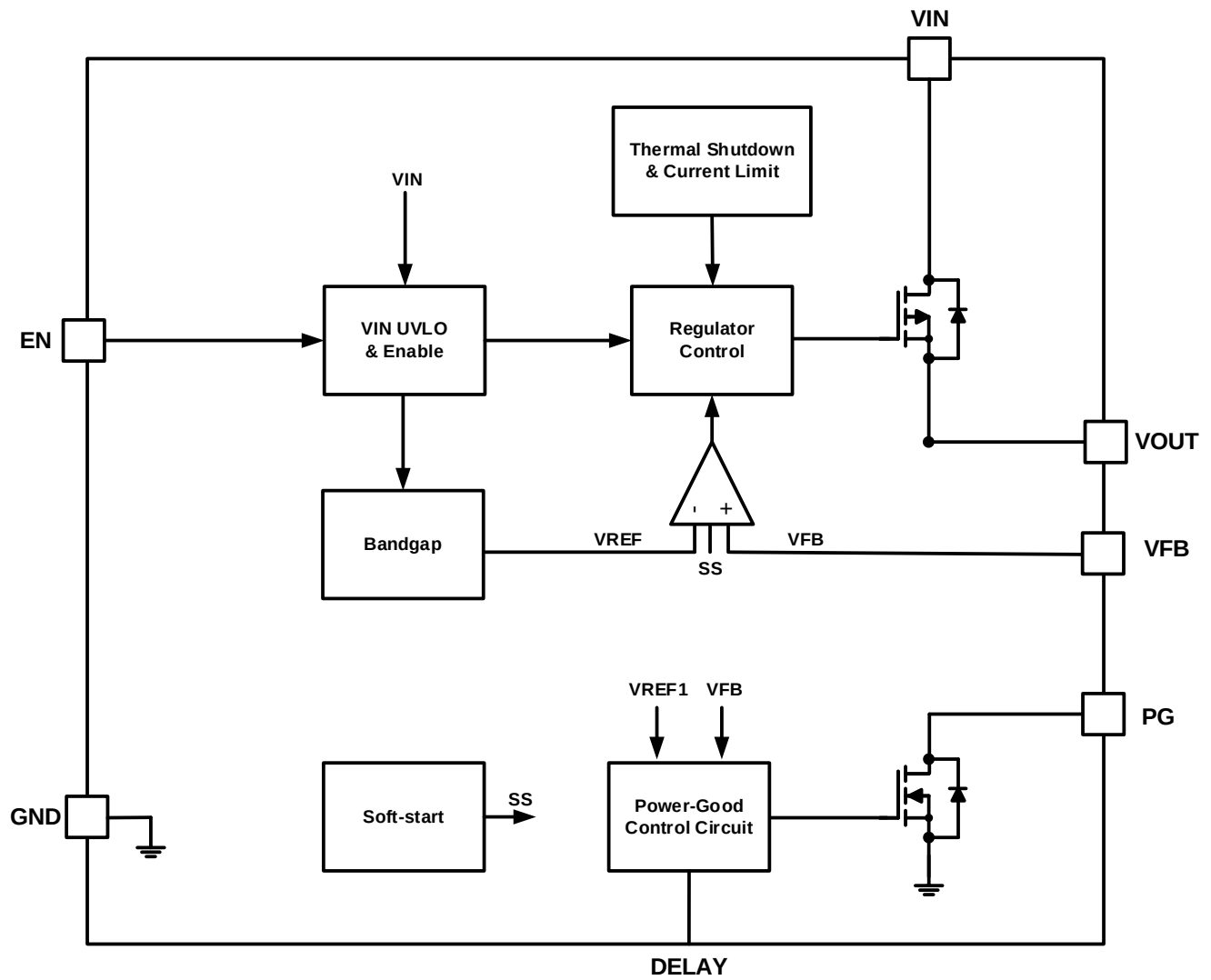
TYPICAL CHARACTERISTICS (continued)



TYPICAL CHARACTERISTICS (continued)



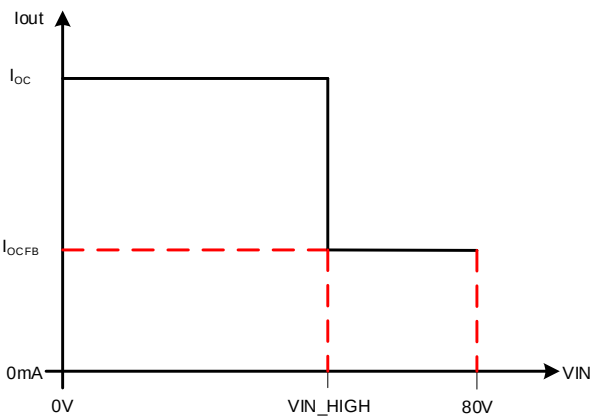
FUNCTIONAL BLOCK DIAGRAM



OPERATION

2 Y H U Y L H Z

VIN_HIGH Control

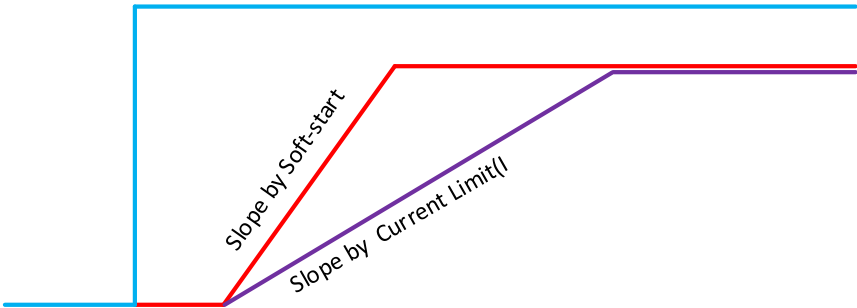


, Q W H U Q 6 0 6 B W W

VIN_HIGH

VIN_HIGH

$$T_{start} = \max \left\{ \frac{x}{(-)}, \quad \right\}$$



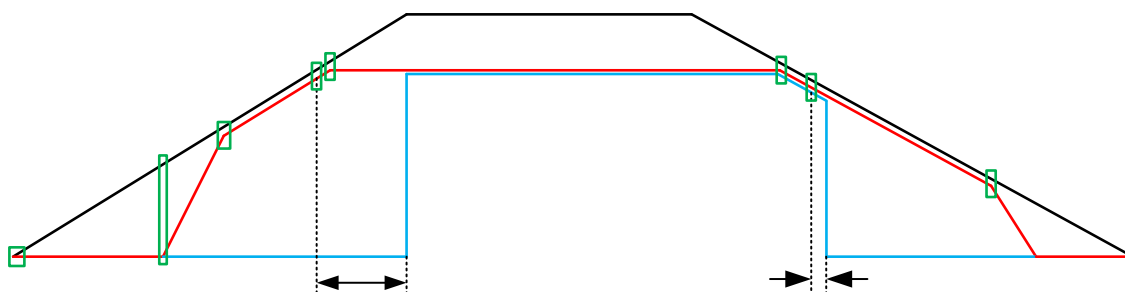
3 R Z H * U R R G D Q G 3 * R Z H G U ' H O D \

%

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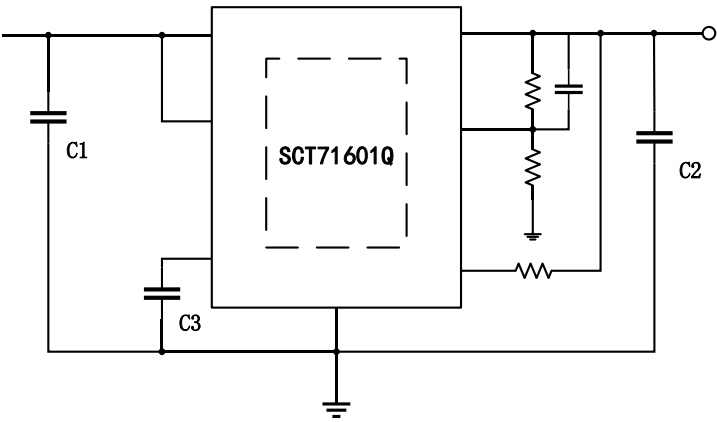
. When C_{DELAY} is used, the PG output is high-impedance when V_{OUT} exceeds V_{IT} , and V_{DELAY} exceeds V_{REF} . The power-good delay time can be calculated using: $t_{DELAY} = (C_{DELAY} \times V_{REF}) / I_{DELAY}$. For example, when $C_{DELAY} = 10 \text{ nF}$, the PG delay time is approximately 12 ms; that is, $(10 \text{ nF} \times 1.2\text{V}) / 1 \mu\text{A} = 12 \text{ ms}$.



7KHUPDO 6KXWGRZQ

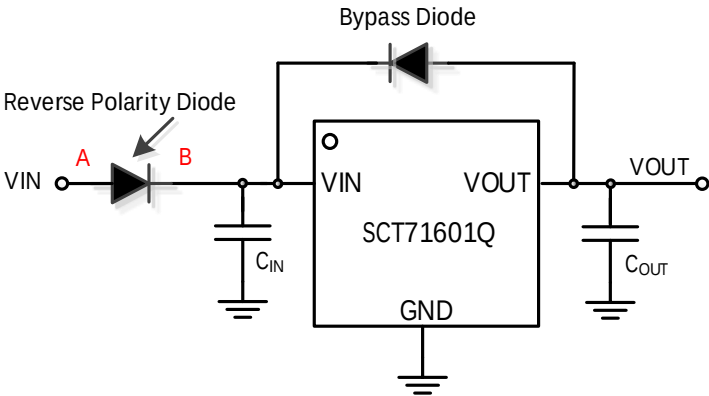
APPLICATION INFORMATION

7\SLFDO DSSOLF DWLRQ



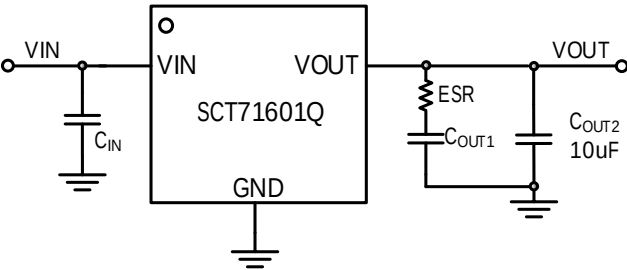
'HVLJQ 3DUDPHWHUV	
'HVLJQ 3DUDPHWHUV	([DP SOH 9DOXH

7\SLFDO DSSOLF DWLRQ

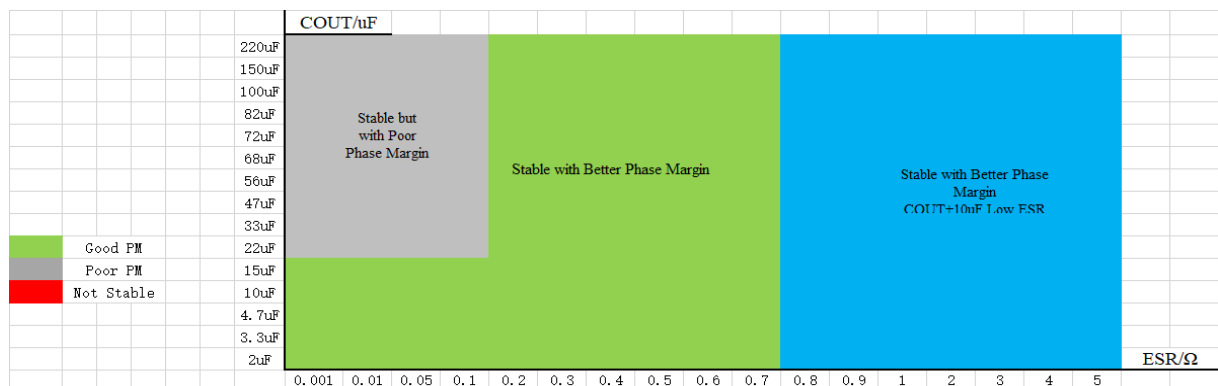
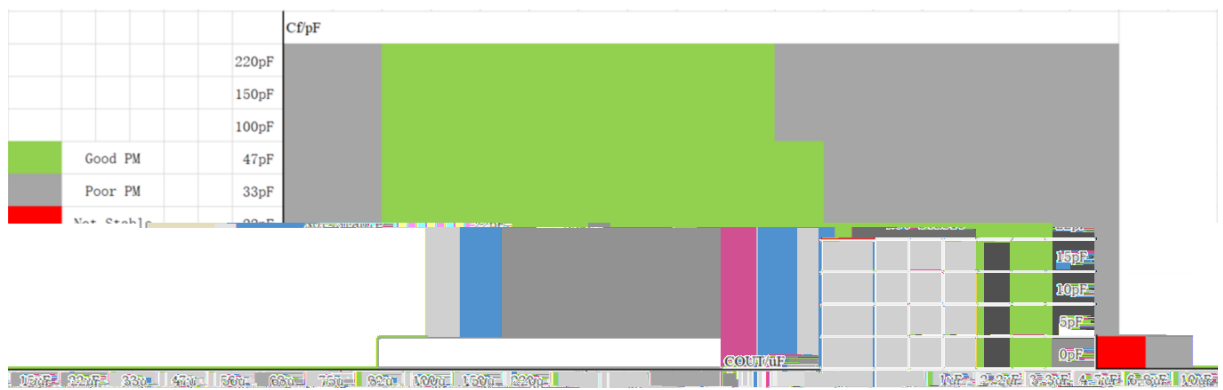
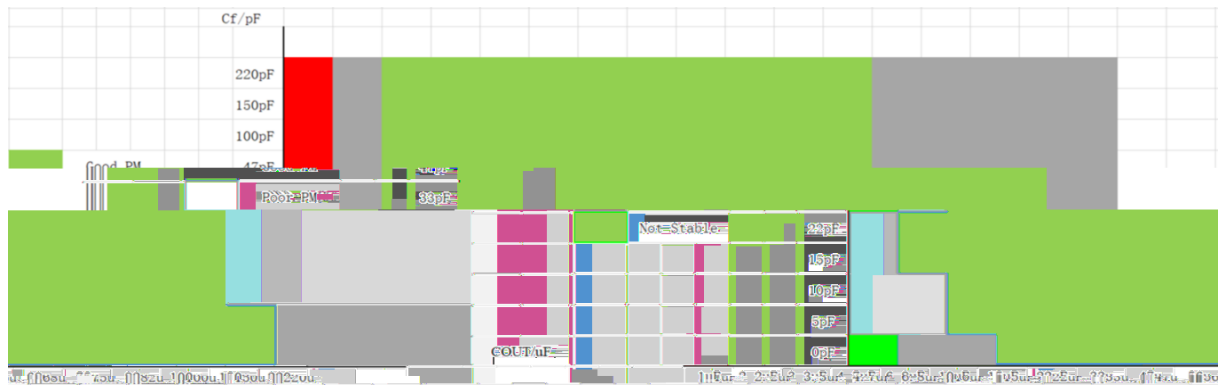


'HVLJQ 3DUDPHWHUV	
'HVLJQ 3DUDPHWHUV	([DPSOH 9DOXH

7\SLFDO DSSOLFDFWLRQ



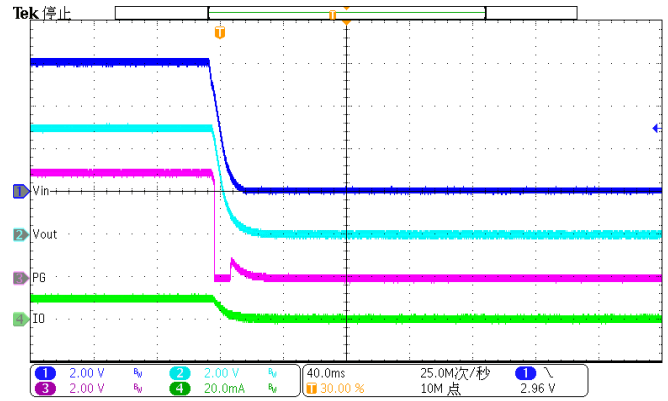
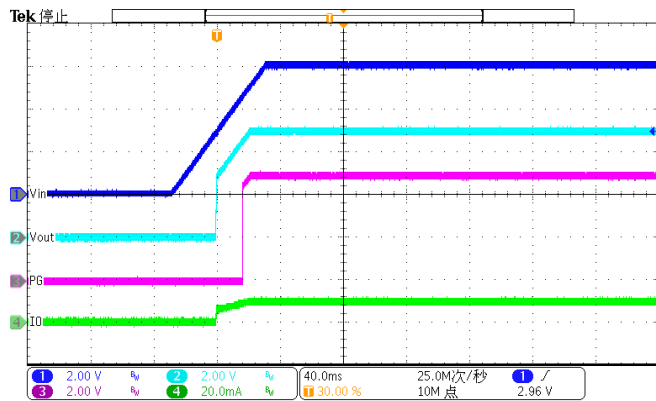
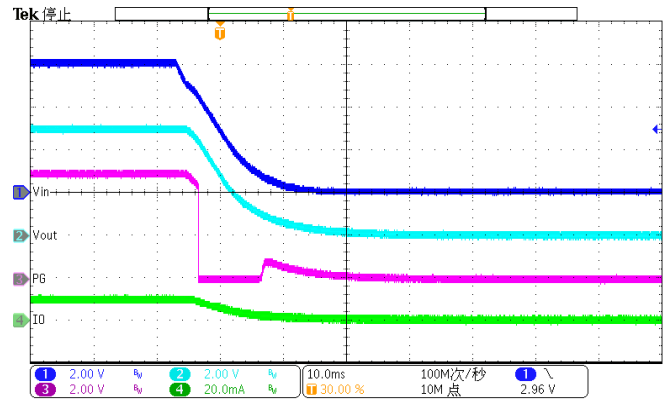
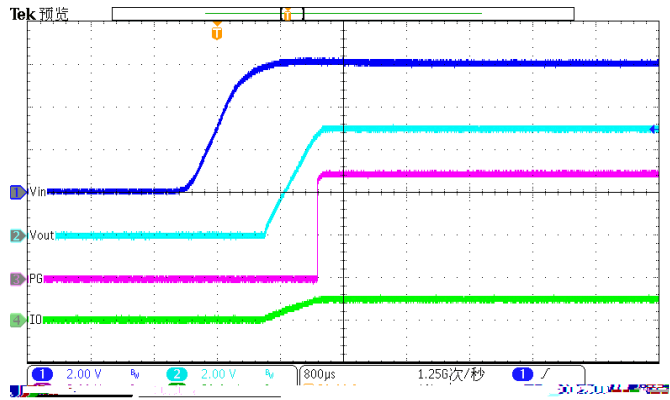
'HVLJQ 3DUDPHWHUV	
'HVLJQ 3DUDPHWHUV	([DPSOH 9DOXH



3RZHU 'LVVLSQWKHUPDQIRUPDQFH

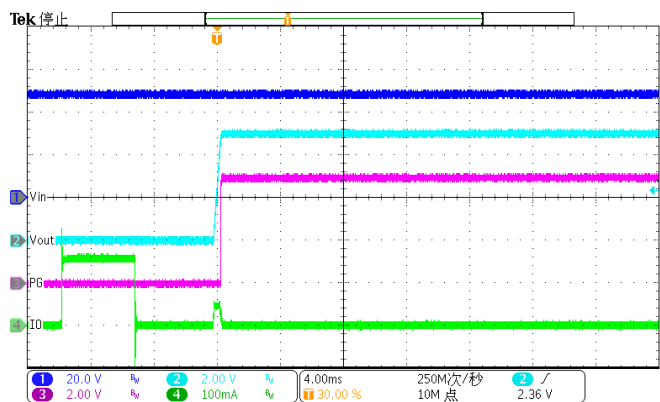
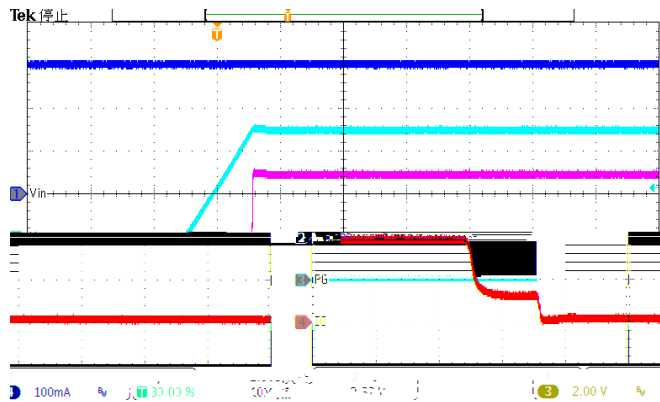
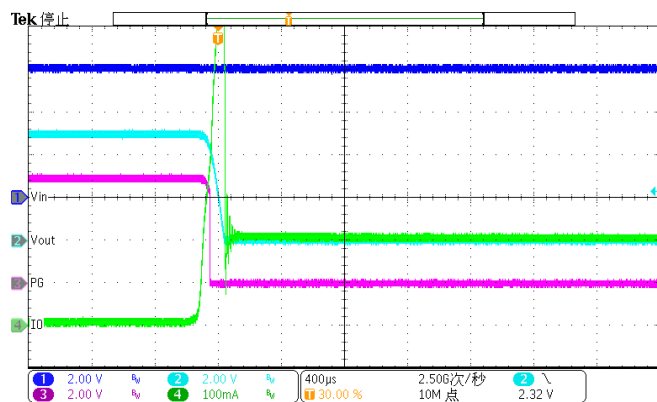
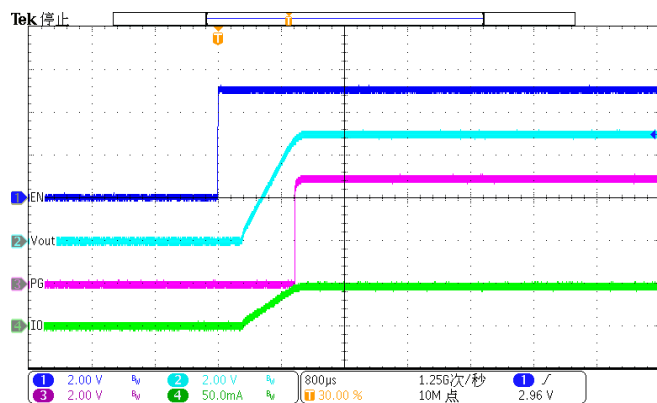
$$= (\quad - \quad) \times \quad + \quad \times$$

\$SSOLFDWLRQ :DYHIRUPV



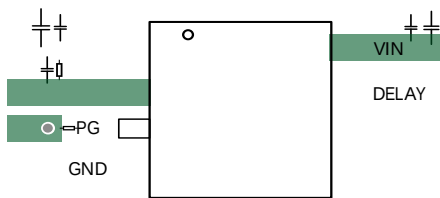
6 & 7 4 6 HULHV

\$SSOLFDWLRQ :DYHIRUPV &RQWLQXH

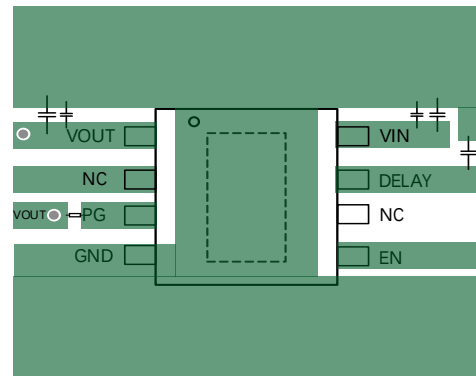


/DIRX*WLGHOLQH

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- 2.
- 3.
- 4.
- 5.



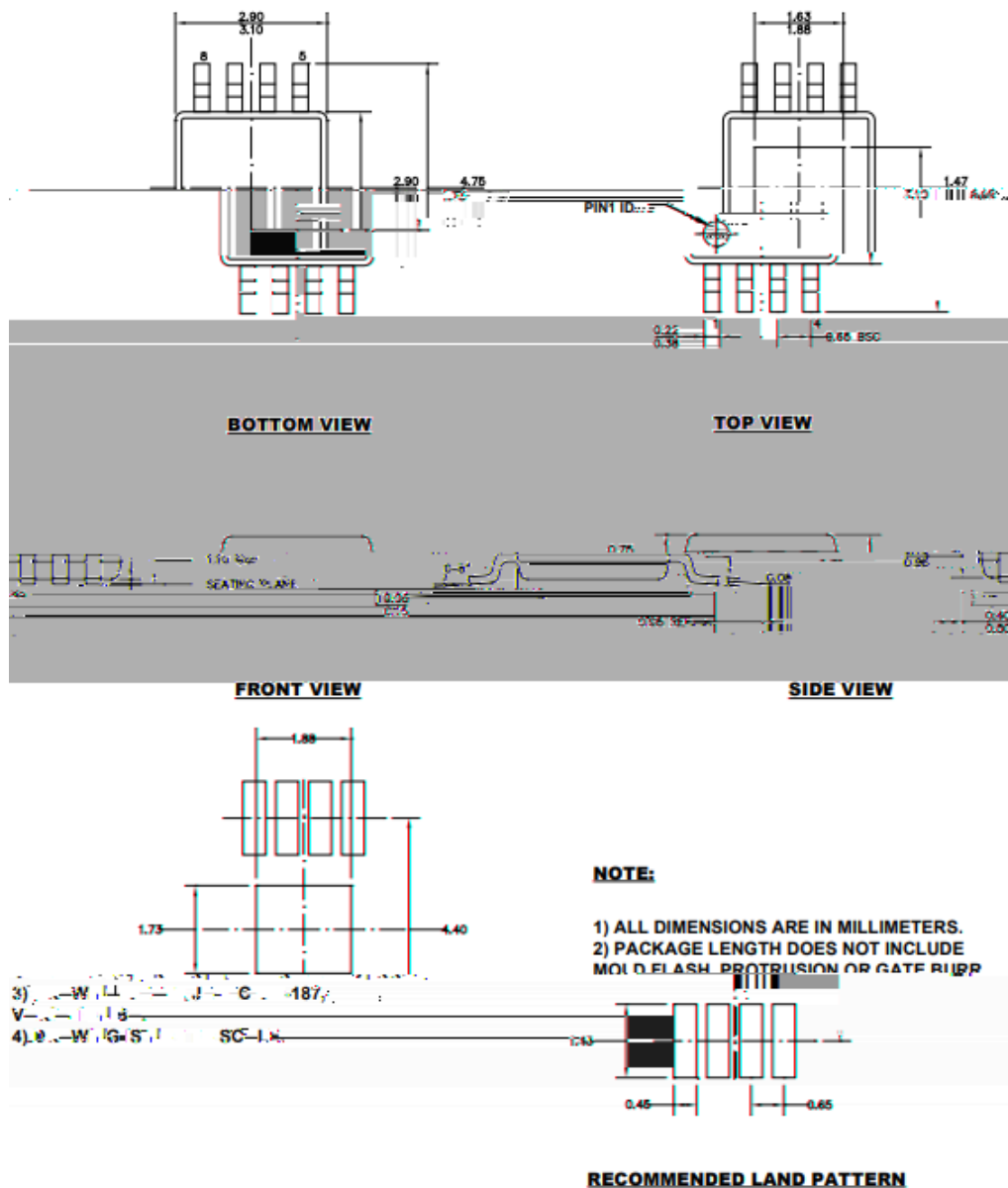
6 & 7 \$ 407(5



6 & 7)[[407(5

PACKAGE INFORMATION

PACKAGE OUTLINE DRAWING FOR MSOP8L-EP POD-0050 Revision 0.0



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- 2.
- 3.
- 4.
- 5.
- 6.

