

N A ? K

? LM ?K

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SCT1271

NAKAGF ALG

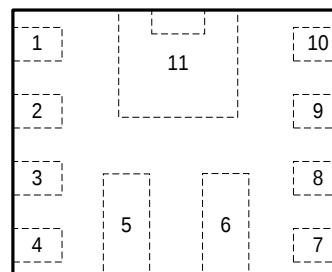
NA G F G E LAGF

ORDERABLE DEVICE	PACKAGING TYPE	STANDARD PACK QTY	PACKAGE MARKING	PINS	PACKAGE DESCRIPTION

KGDML E PAEME LAF K

HAF GF AM LAGF

DESCRIPTION	MIN	MAX	UNIT
			o
			o
			o



HAF MF LAGFK

NAME	NO.	PIN FUNCTION

GE E GF GH LAF GF AAGFK

PARAMETER	DEFINITION	MIN	MAX	UNIT

K LAF K

PARAMETER	DEFINITION	MIN	MAX	UNIT

L E DAF G E LAGF

PARAMETER	THERMAL METRIC	QFN-11L	UNIT

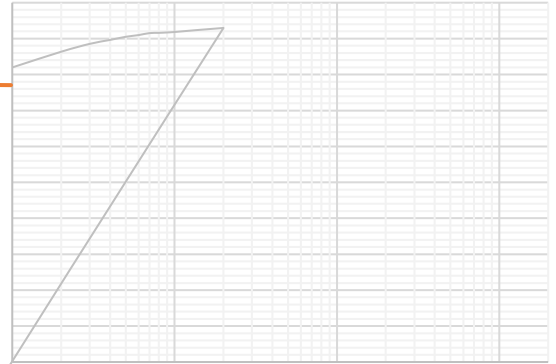
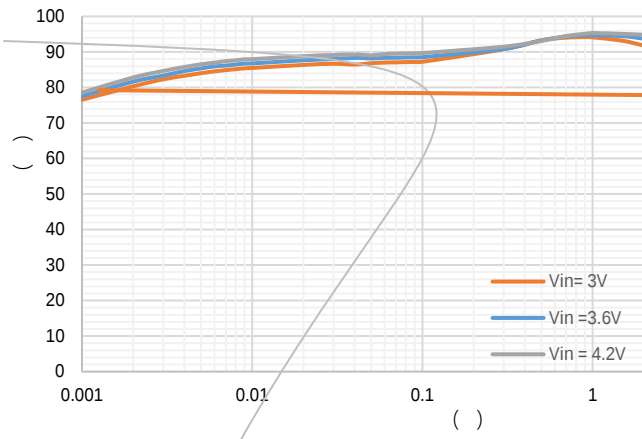
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QUESTION LAID

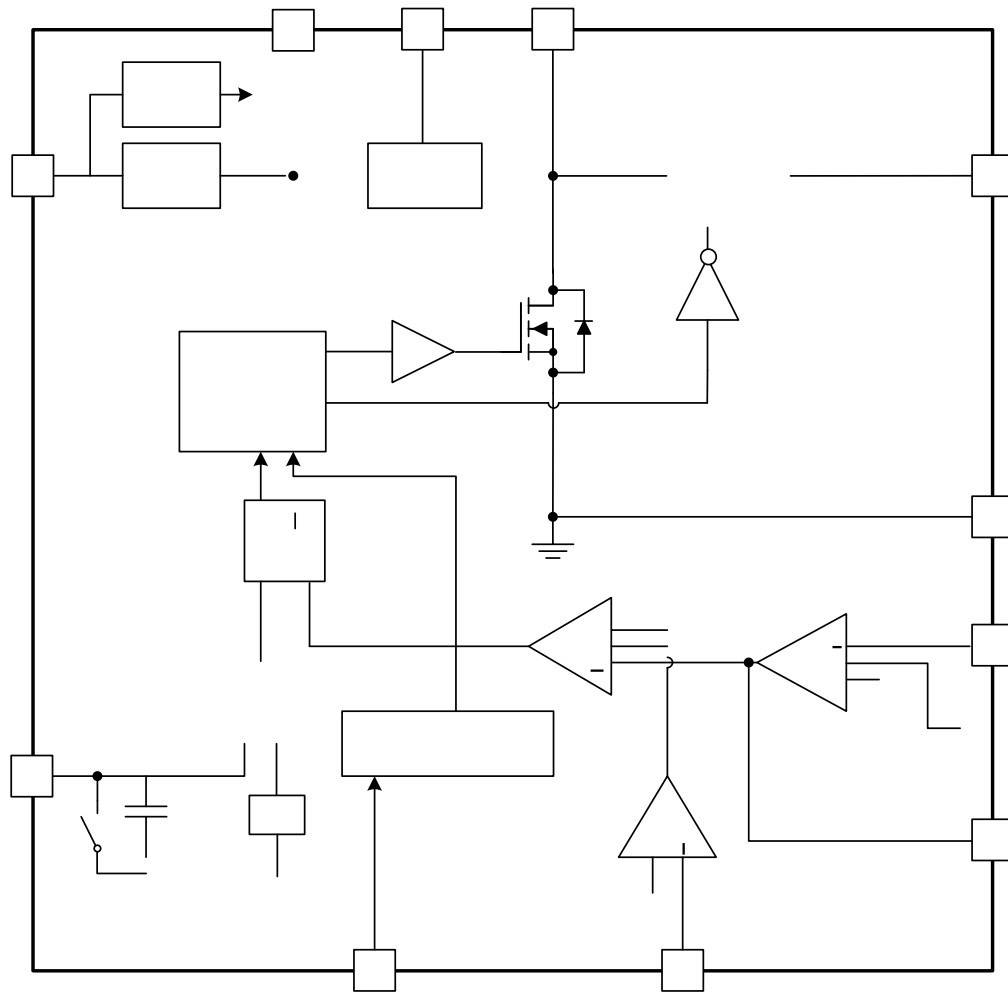
L ? A K L A K

SYMBOL	PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT
Power Supply and Output						
Reference and Control Loop						
Power MOSFETs						
Current Limit						
Enable						
Switching Frequency						
Protection						

L HA D L? ALAK



MF LAGF D DG C A E



GH? LAGF

Overview

Adjustable Peak Current Limit

-
-

Adjustable Switching Frequency

-
-
-
-
-

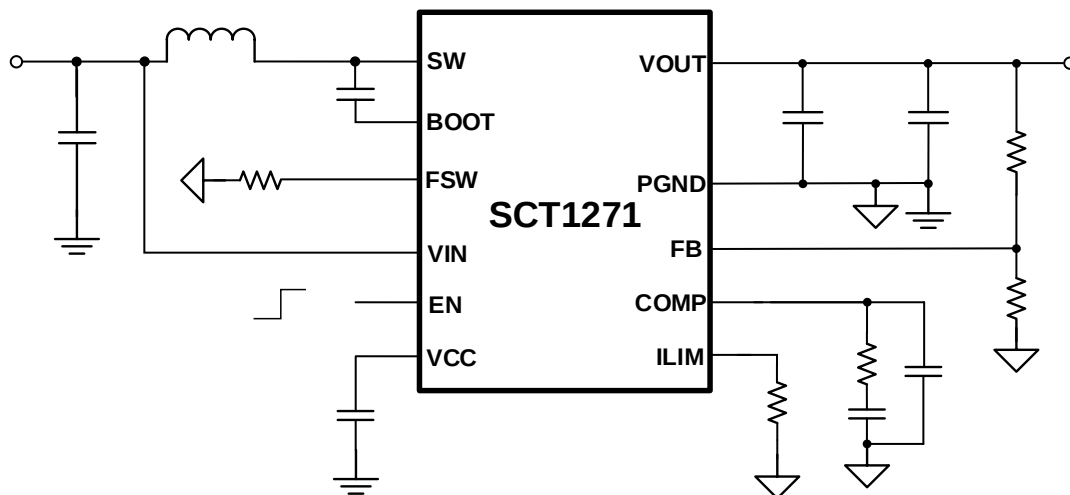
Over Voltage Protection and Minimum On-time

Thermal Shutdown

SCT1271

HHDA LAGF AF G E LAGF

Typical Application



Design Parameters

Design Parameters	Example Value

Switching Frequency

-
-
-
-
-

F _{SW}	R _{FREQ}

Peak Current Limit

-
-

I _{LIM}	R _{LIM}

Output Voltage

-

V _{OUT}	R ₁	R ₂

Inductor Selection

Table 4. Recommended Inductors

Part Number	L (uH)	DCR Max (mΩ)	Saturation Current/Heat Rating Current (A)	Size Max (LxWxH mm)	Vendor

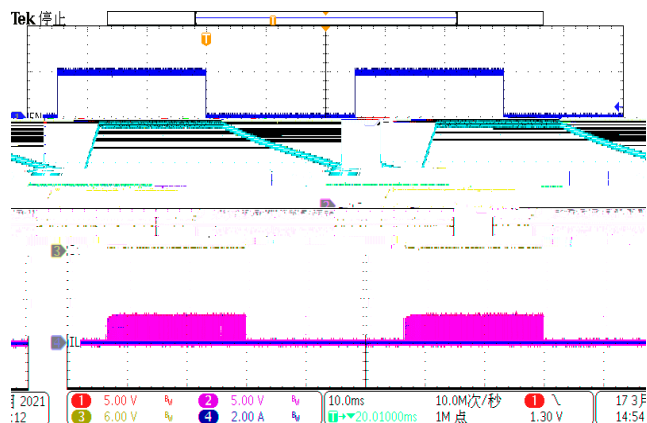
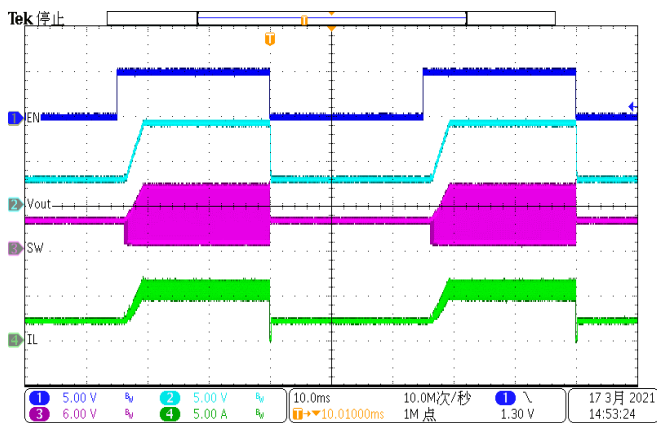
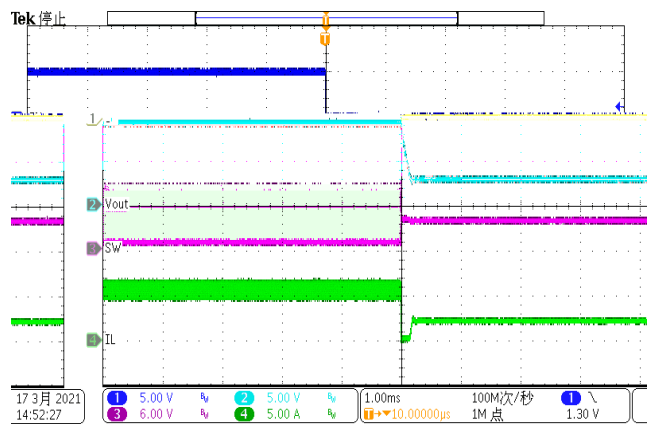
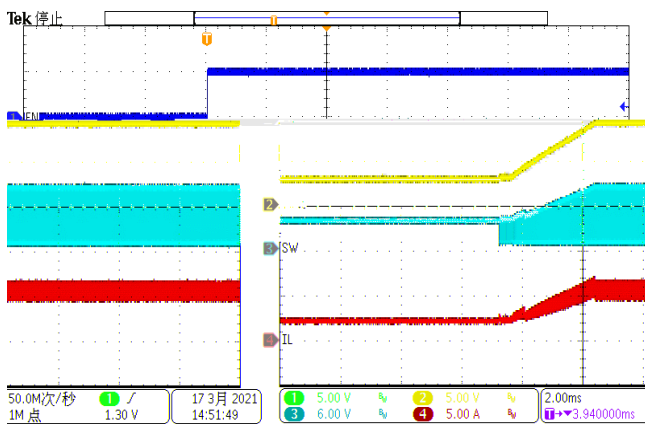
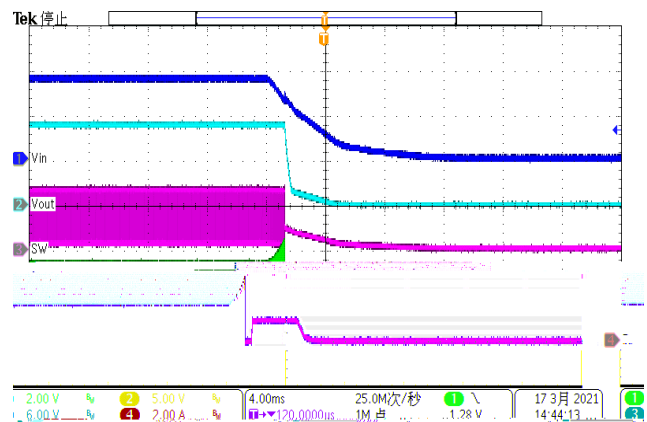
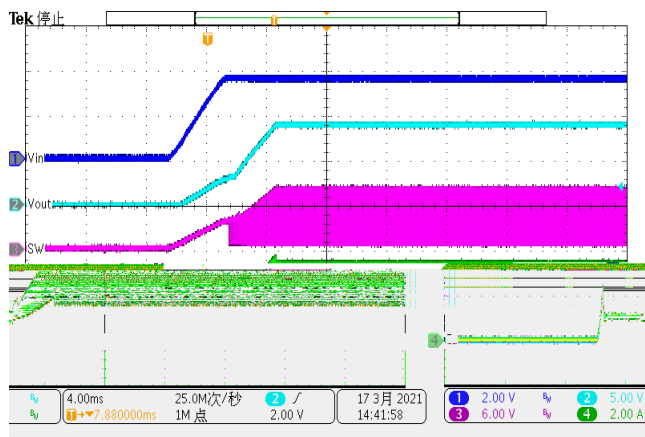
Input Capacitor Selection

Output Capacitor Selection

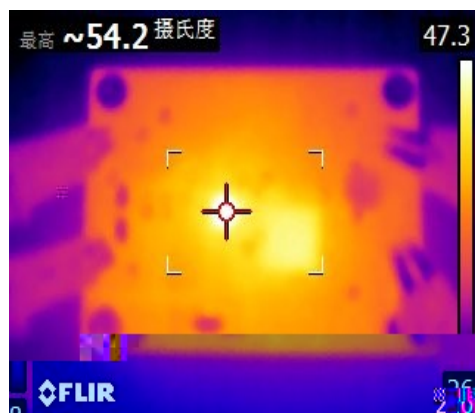
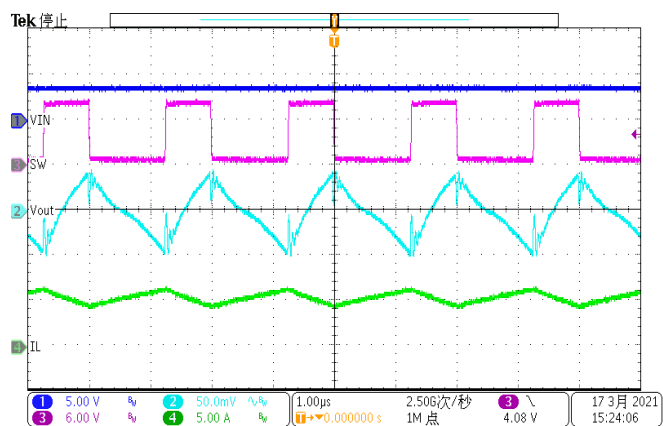
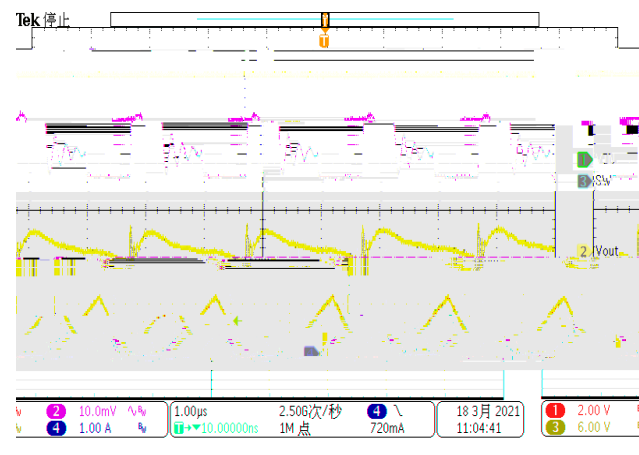
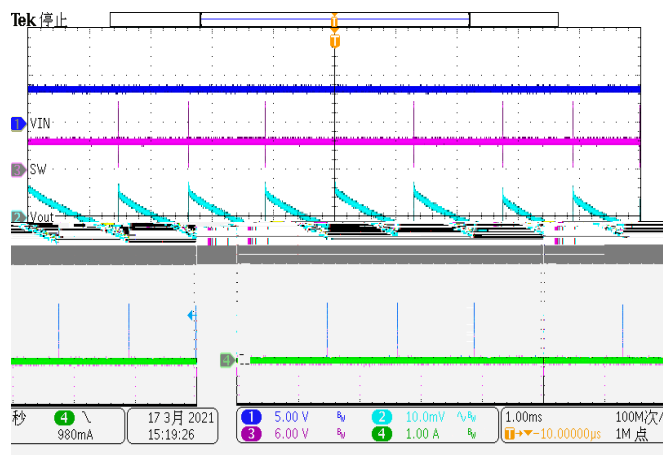
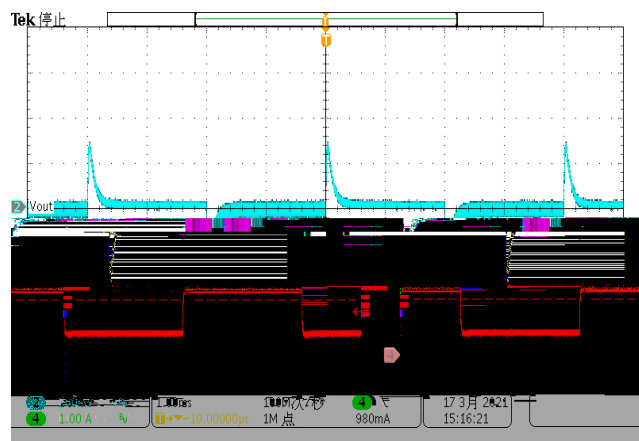
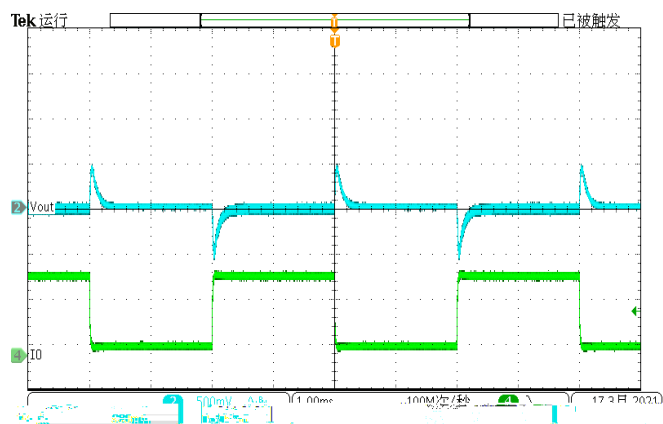
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Loop Stability

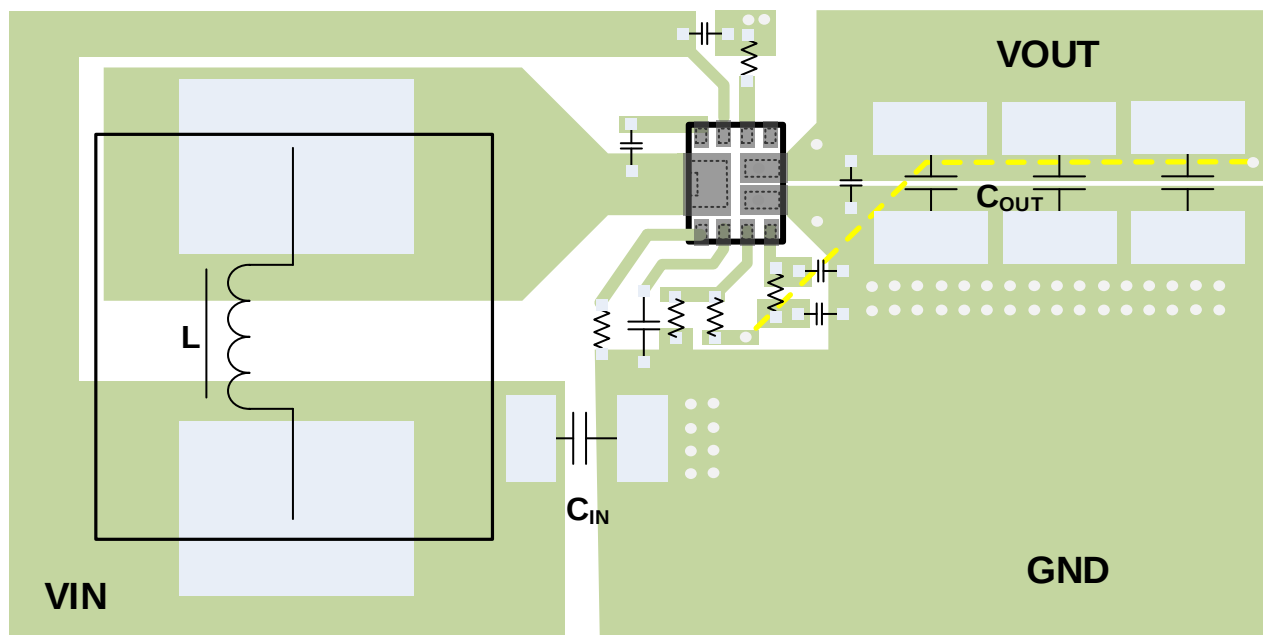
Application Waveforms



Application Waveforms(continued)



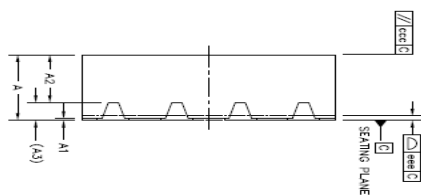
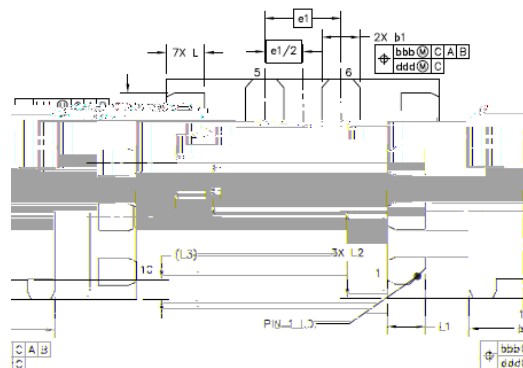
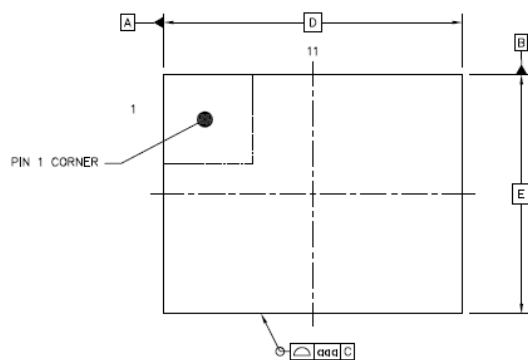
Layout Guideline



Thermal Considerations

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H C ? AF G E LAGF



NOTE:

[illegible]

L H2 F 22DAF G E LAGF

